

Instructor: Cheng Chin

Lectures: Tue & Thur 11:00-12:20 pm KPTC 105

Office hour: Thur 12:30-01:30 pm GCIS E107

Email: cchin@uchicago.edu (please add *Phy226* in the subject)

Class notes: http://ultracold.uchicago.edu/Spring_2018_P226

Teaching Assistants:

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Textbooks: Learning the Art of Electronics by *T. Hayes and P. Horowitz*
(Recomm.) The Art of Electronics, 3rd Ed., *P. Horowitz and W. Hill*

Lab sessions: KPTC 208 (Mon & Wed) or (Tue & Thur) 1:30-5:20 pm
Lab reports should be turned in by the end of the sessions.
Attendance of all lab sessions is required.

Grades: 14 Lab reports: 70%; Midterm exam: 15%; Final exam: 15%

Wk	Date	Lecture	Lab	Report
1	3/27	DC circuits	----- no lab -----	
	3/29	Capacitors and inductors	----- no lab -----	
2	4/3	Diodes	Lab 1: DC circuits	1
	4/5	P-N physics	Lab 2: Capacitors	2
3	4/10	Transistors I	Lab 3: Diodes	3
	4/12	Transistors II	Lab 4: Transistors I	4
4	4/17	Operational amplifiers I	Lab 5: Transistors II	5
	4/19	<i>EFL electronics shop tour</i>	Lab 6: Op-amp I	6
5	4/24	Operational amplifiers II	Lab 7: Op-amp II	
	4/26	Feedback Theory	Lab 7: Op-amp III	7
6	5/1	Midterm (lec. 1~10)	Temperature Servo I	8
	5/3	Optoelectronics	Temperature Servo II	
7	5/8	Optical communication	Temperature Servo III	9
	5/10	Analog meets digital	Lab 13: Group Audio I	10
8	5/15	Gates	Lab 13: Group Audio II	11
	5/17	Flip-flops	Lab 14. Gates	12
9	5/22	Counters	Lab 15. Flip-Flops	13
	5/24	FPGA	FPGA I	
10	5/29	<i>Electronics in research</i>	FPGA II	14
	5/31	Final (lec. 11~17)		